

FIG. 1

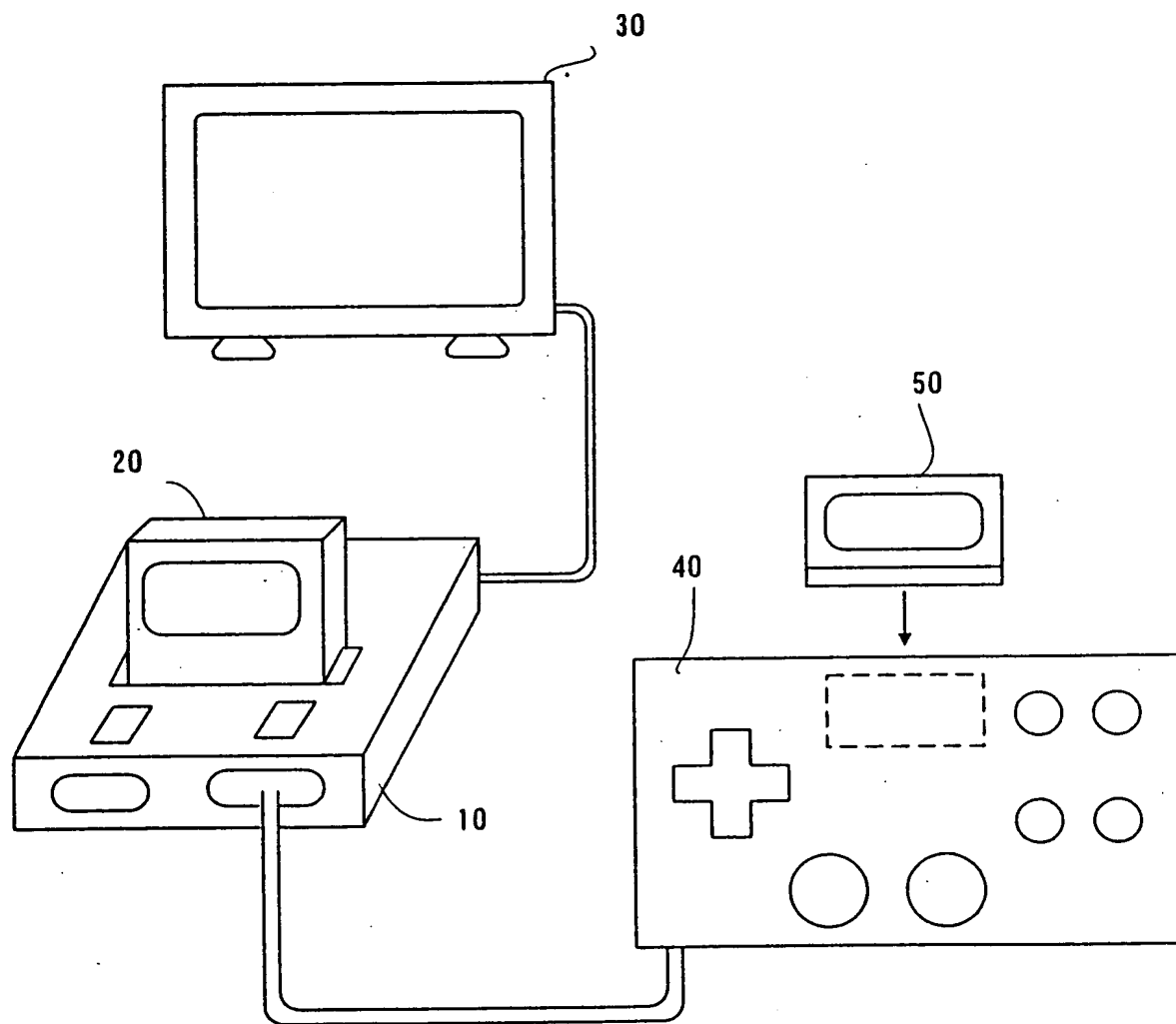


FIG. 2

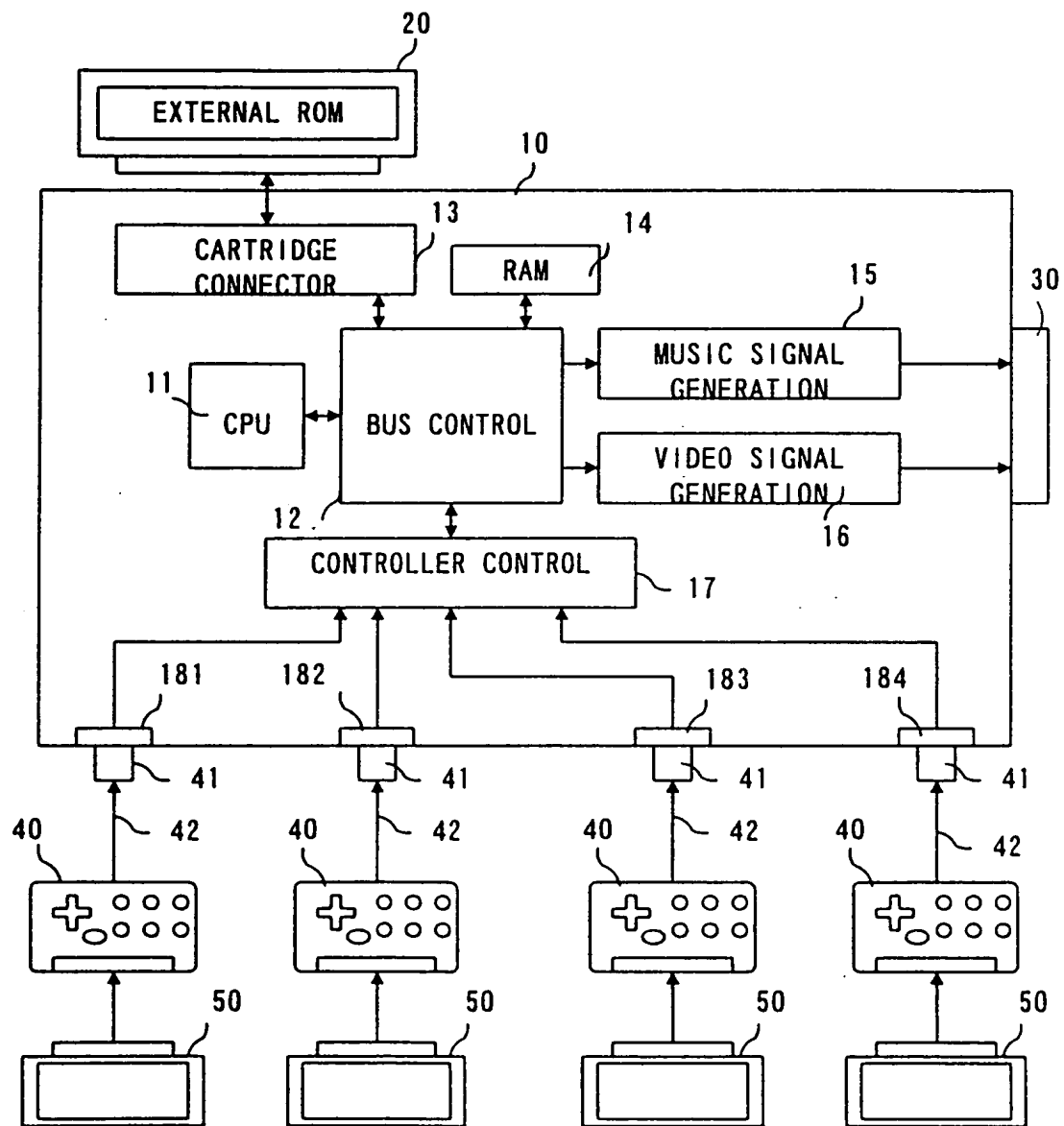
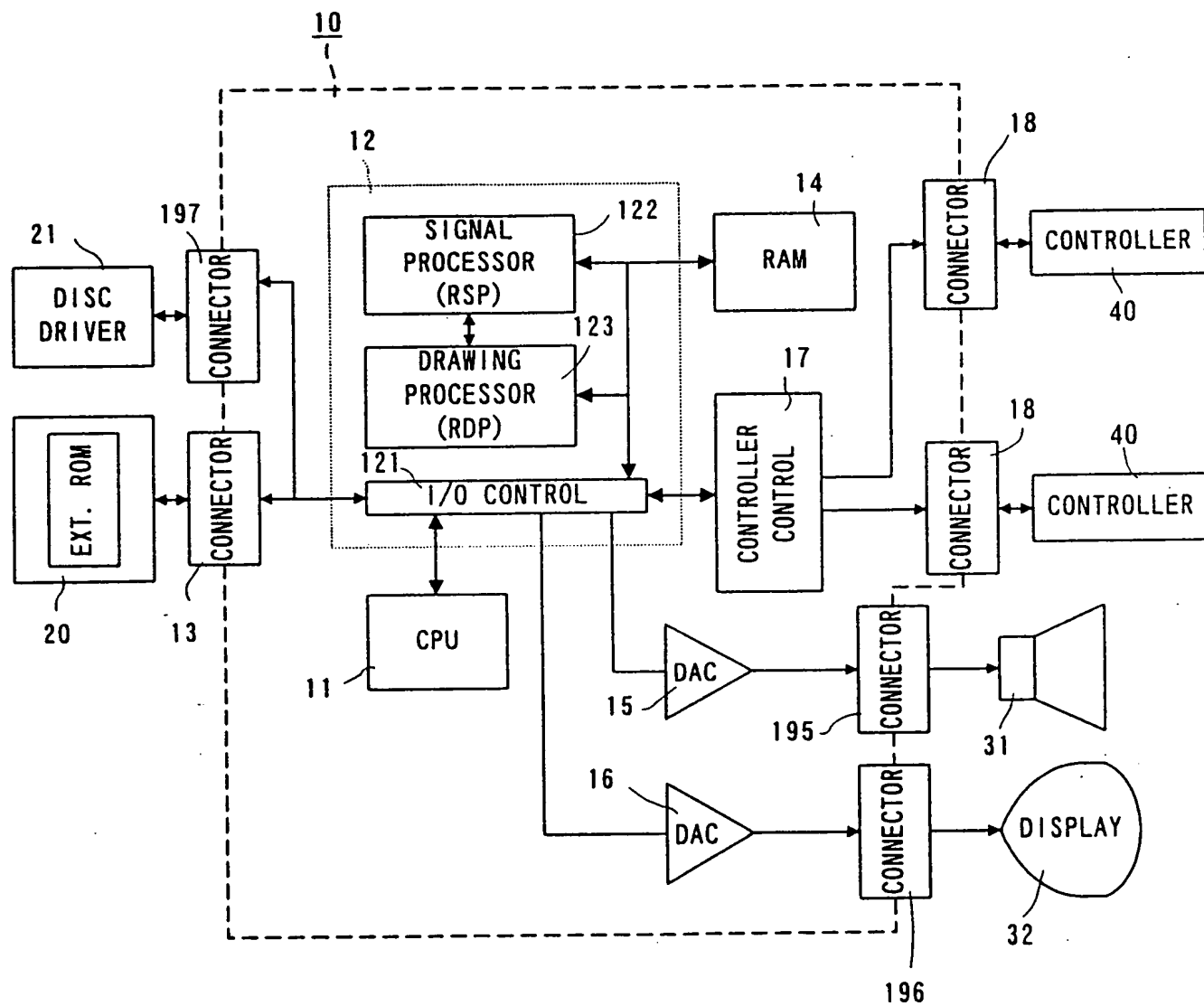


FIG. 3



RAM 14

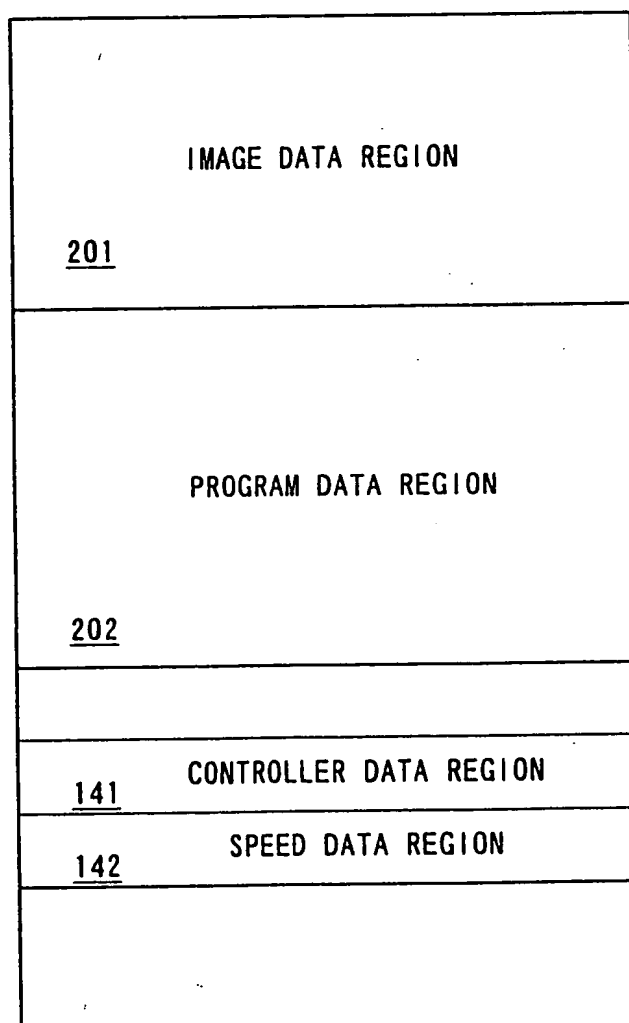


FIG. 5

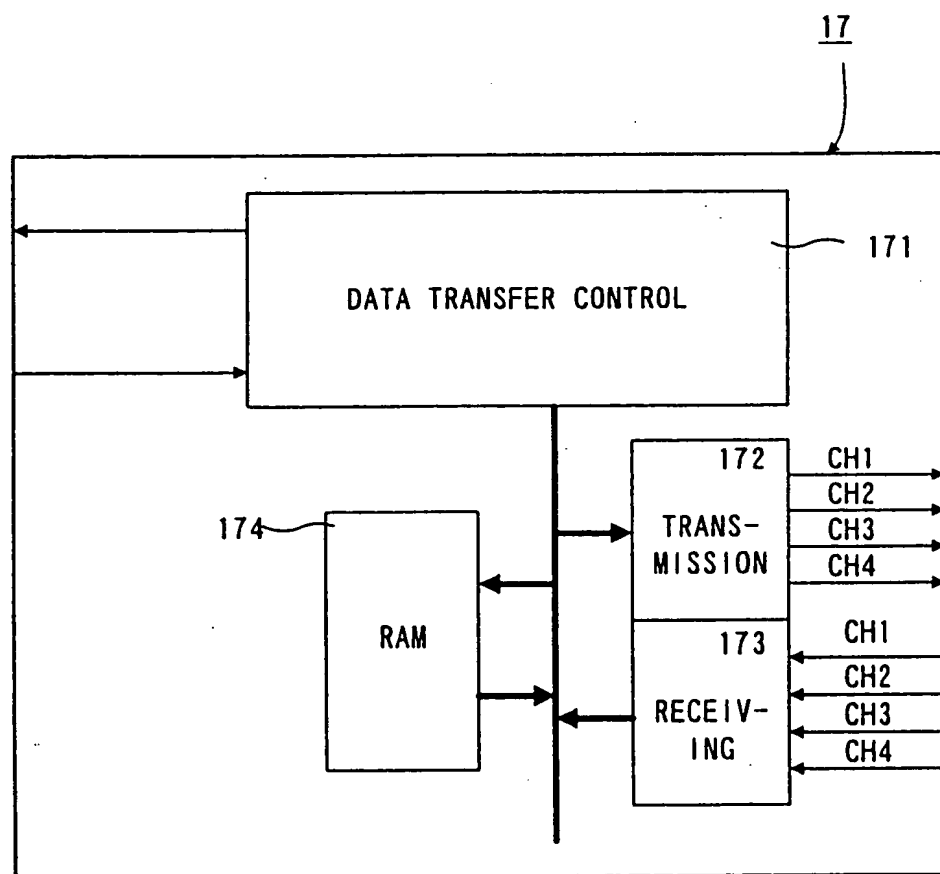


FIG. 7

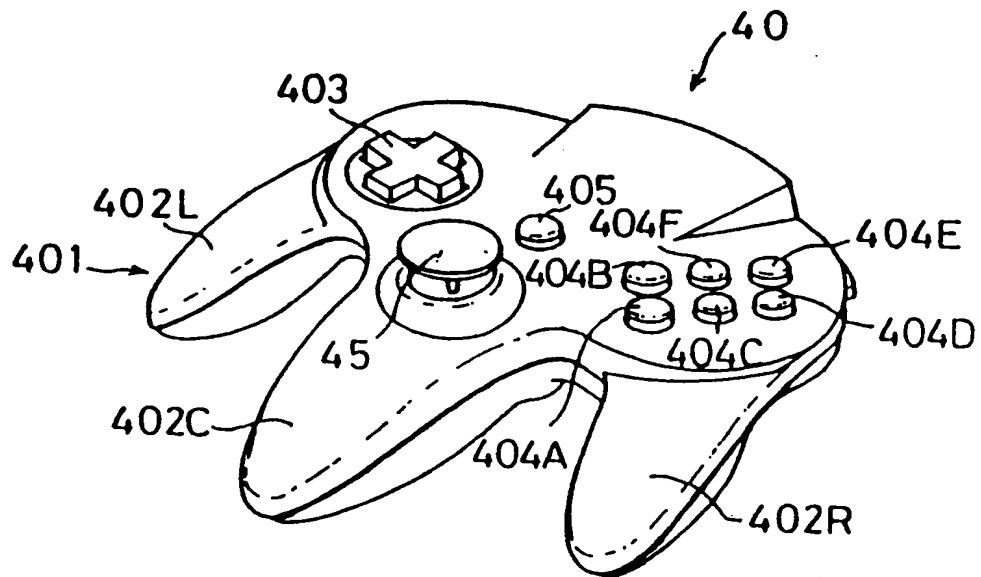


FIG. 8

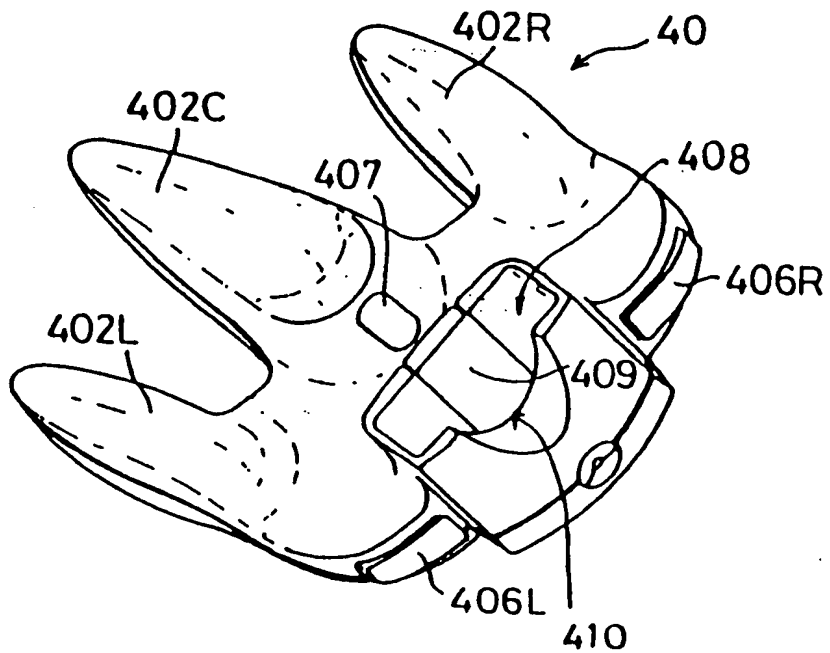
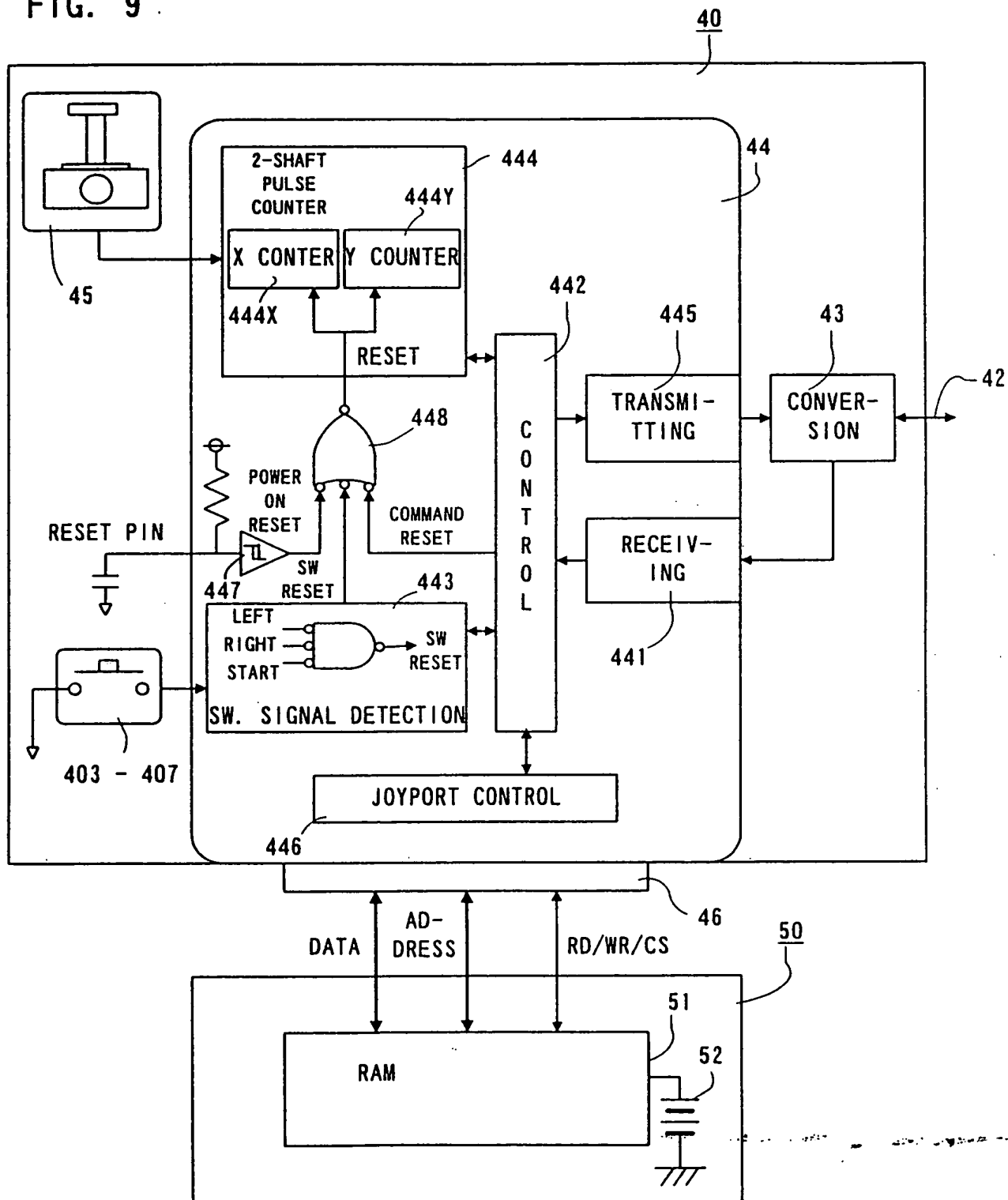


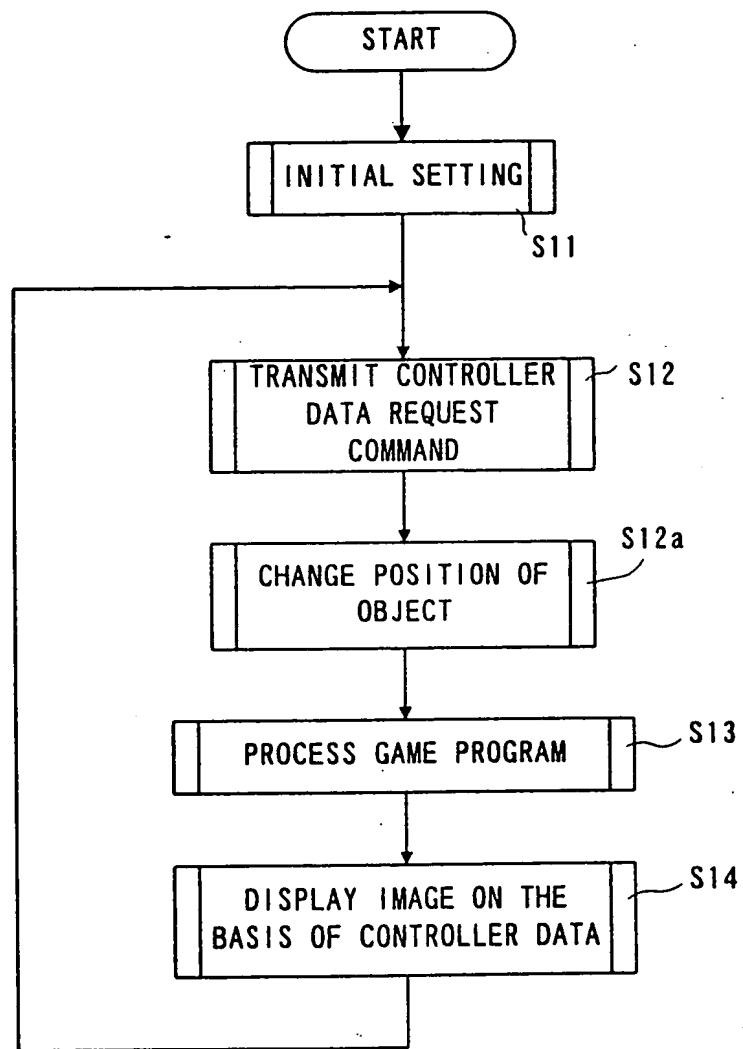
FIG. 9



[illegible]

1 BYTE	B	A	G	START	↑	↓	←	→
2 BYTE	JSRST	O	L	R	E	D	C	F
3 BYTE	← X ORDINATE →							
4 BYTE	← Y ORDINATE →							

FIG. 11



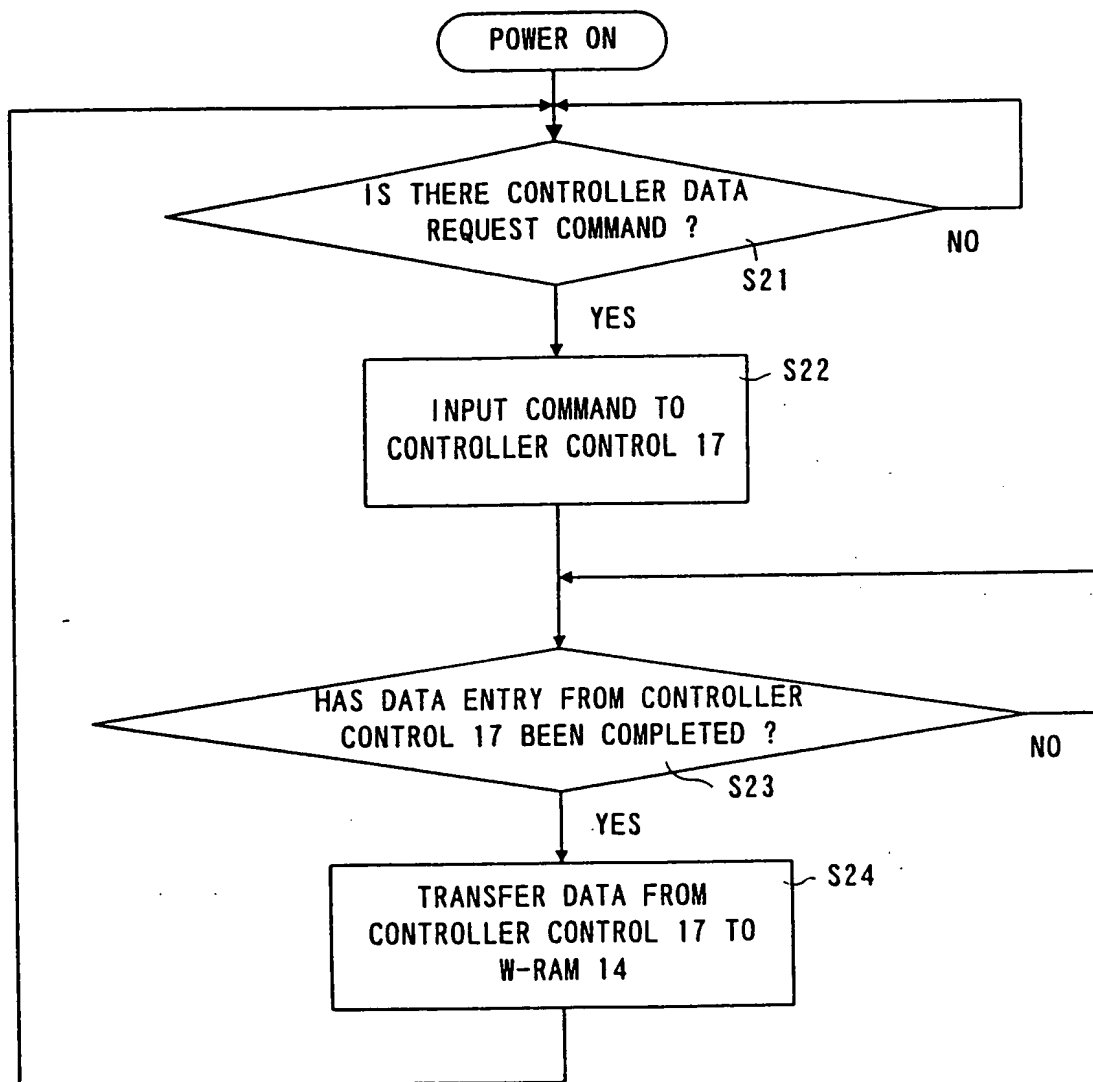


FIG. 13

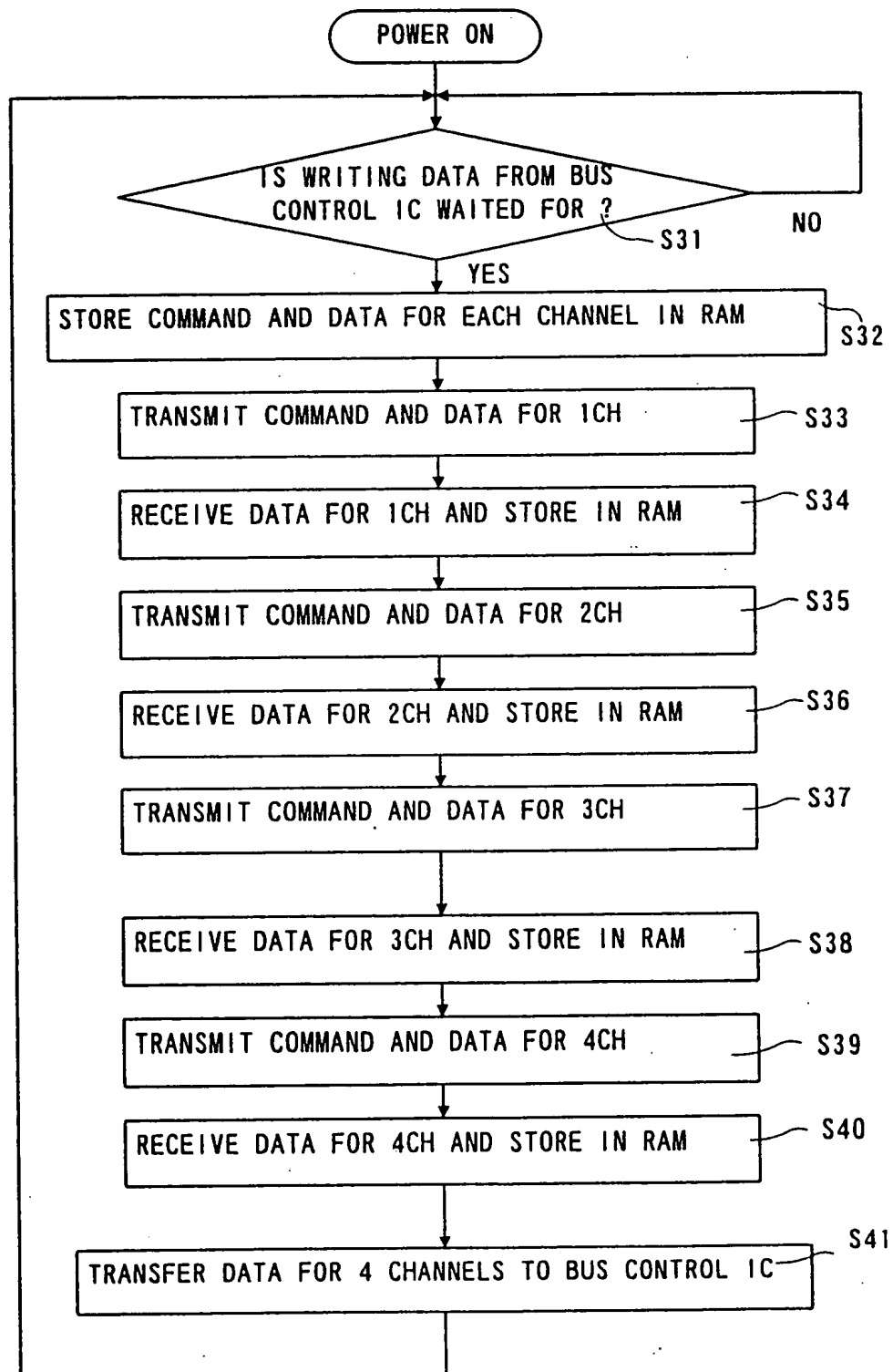


FIG. 14

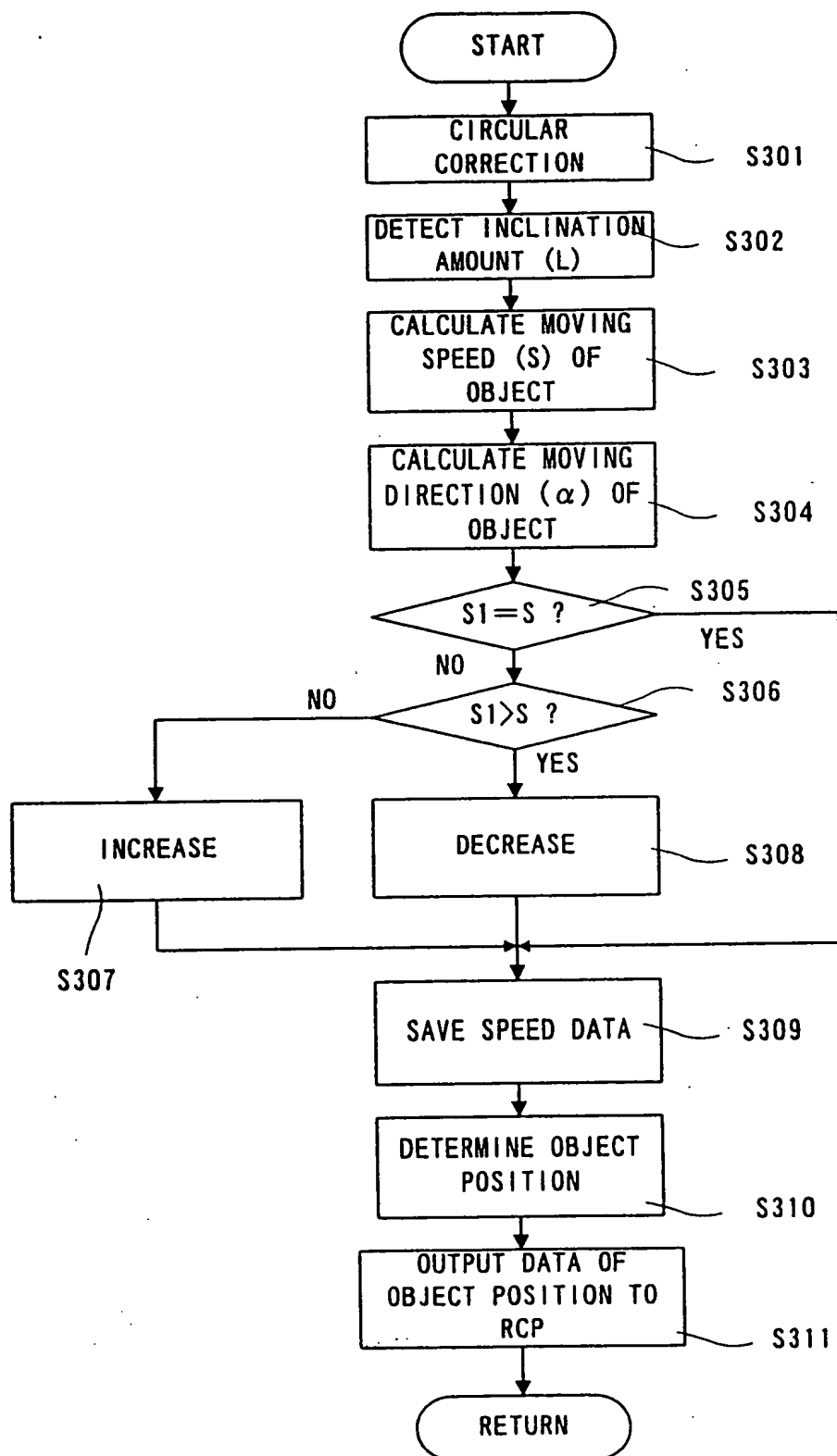


FIG. 15

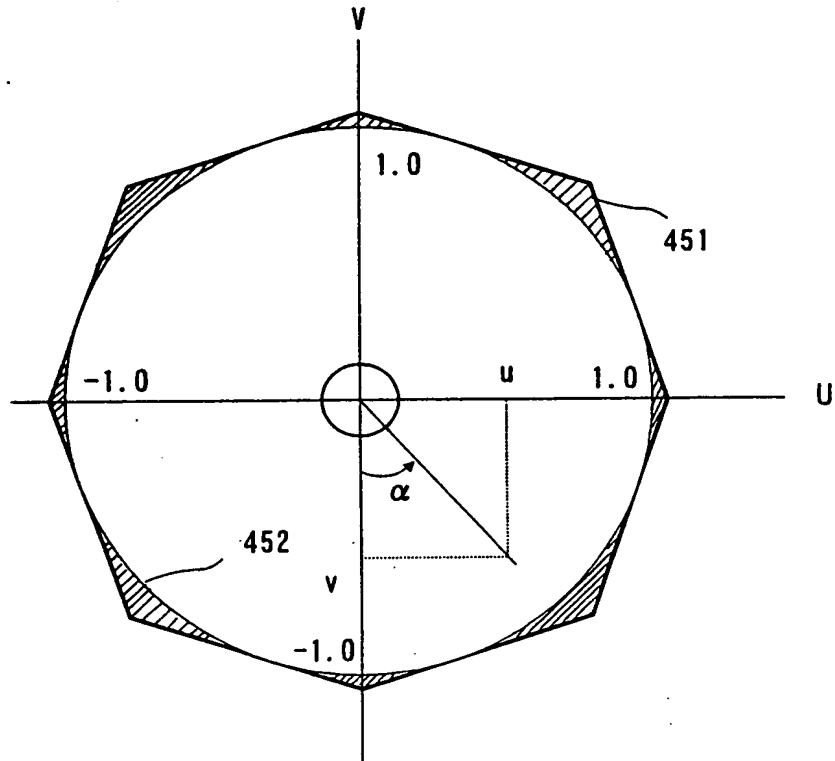


FIG. 16

